

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): F.A. Baiocchi et al.

Case: 3-4-42-11

Serial No.: To Be Assigned

Filing Date: March 31, 2004

Group: To Be Assigned

Examiner: To Be Assigned

Title: Enhanced Substrate Contact for a Semiconductor Device

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying Form PTO-1449: C.S. Kim et al., "Trenched-Sinker LDMOSFET (TS-LDMOS) Structure for 2 GHz Power Amplifiers," ETRI Journal, Vol. 25, No. 3, pp. 195-202, June 2003. A copy of the listed document is enclosed.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,



Date: March 31, 2004

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FORM PTO-1449 (MODIFIED)**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

Applicant(s): F.A. Baiocchi et al.
Case: 3-4-42-11
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Group: TBA

U.S. PATENT DOCUMENTS

EXAMINER	INITIAL	DOCUMENT NO.	DATE	NAME	FILING DATE
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FILING DATE

IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER	INITIAL	DOCUMENT NO.	DATE	COUNTRY	TRANSLATION
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CLASS/SUBCLASS

YES

NO

OTHER DOCUMENTS

EXAMINER	INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
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1. C.S. Kim et al., "Trenched-Sinker LDMOSFET (TS-LDMOS) Structure for 2 GHz Power Amplifiers," ETRI Journal, Vol. 25, No. 3, pp. 195-202, June 2003.

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.